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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/748,328	12/31/2003	Noriaki Setoguchi	122.1372C	8118
21171	7590	06/15/2005	EXAMINER	
STAAS & HALSEY LLP SUITE 700 1201 NEW YORK AVENUE, N.W. WASHINGTON, DC 20005			LEE, WILSON	
			ART UNIT	PAPER NUMBER
			2821	

DATE MAILED: 06/15/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/748,328	SETOGUCHI ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	Wilson Lee	2821	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

1)  Responsive to communication(s) filed on 07 January 2005.

2a)  This action is FINAL.                            2b)  This action is non-final.

3)  Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## **Disposition of Claims**

4)  Claim(s) 4 and 8-13 is/are pending in the application.  
4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5)  Claim(s) 8-10 is/are allowed.

6)  Claim(s) 4 and 12 is/are rejected.

7)  Claim(s) 11 and 13 is/are objected to.

8)  Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

9)  The specification is objected to by the Examiner.

10)  The drawing(s) filed on \_\_\_\_\_ is/are: a)  accepted or b)  objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)  The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a)  All b)  Some \* c)  None of:  
1.  Certified copies of the priority documents have been received.  
2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3.  Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

1)  Notice of References Cited (PTO-892)  
2)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  
3)  Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 12/03, 10/04, 1/05.

4)  Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.

5)  Notice of Informal Patent Application (PTO-152)

6)  Other: \_\_\_\_\_

### **Claim Rejections – 35 U.S.C. 102**

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

Claims 4 and 12 are rejected under 35 U.S.C. 102(e) as being anticipated by Nagai (6,243,084).

Regarding Claim 4, Nagai discloses a method for driving a plasma display panel having parallel, first and second electrodes comprising:

- performing a reset discharge, an addressing discharge for writing display data and a sustain discharge for allowing a display image to glow, according to the display data, wherein the reset discharge comprises a first discharge induced by a first pulse (V<sub>p</sub>) in which an applied voltage varies with time in a positive direction and a second discharge induced by a second pulse (V<sub>pp</sub>) in which an applied voltage varies with time in a

- negative direction (reverse polarity) (See Abstract and Col. 1, line 60 to Col. 2, line 25, Col. 5, lines 33-63, Col. 7, line 67 to Col. 8, line 16) and the first discharge ( $V_p$ ) is induced by the first pulse applied to the second electrode ( $Y_1$ ) and which rises from a predetermined potential (the voltage level of  $V_p$ ) in the positive direction while the first electrode ( $X$ ), arranged in parallel to the second electrode, is set to a lower potential than the predetermined potential (the voltage level of  $V_{pp}$ ) (See Col. 1, line 60 to Col. 2, line 25, Col. 5, lines 33-63, Col. 7, line 67 to Col. 8, line 16, Figure 2 and Abstract).

Regarding Claim 12, Nagai discloses a method for driving a plasma display panel, comprising: performing a reset discharge, an addressing discharge for writing display data and a sustain discharge for allowing a display image to glow, according to the display data, wherein:

- the reset discharge includes a first discharge induced by a first pulse (first voltage) in which an applied voltage varies with time in a first direction and a second discharge induced by a second pulse (fifth voltage, pre-priming pulse) in which an applied voltage varies with time in a second direction (reverse polarity), opposite to the first direction (See Col. 1, line 60 to Col. 2, line 25, Col. 5, lines 33-63, Col. 7, line 67 to Col. 8, line 16, Figure 2 and Abstract),
- the applied voltage of said first pulse (priming voltage, 121,  $V_p$ , or first voltage) rises until reaching a first potential (e.g. the voltage level of  $V_p$ )

and the first pulse has a predetermined polarity with respect to a second potential (124, V<sub>pp</sub> or the fifth voltage), attained before the application of the first pulse (124 attained before V<sub>p</sub> because it is a pre-priming pulse).

See Abstract and figure 2), and

- the second pulse (124, V<sub>pp</sub> or fifth voltage) has a polarity opposite to the predetermined polarity of the first pulse with respect to the second potential (the voltage level of V<sub>pp</sub>) and is applied before the first potential (the voltage level of V<sub>p</sub>) (e.g. 124 is a pre-priming pulse which is applied before the first potential) is lowered to said second potential (the voltage level of V<sub>pp</sub>) (See Figure 2).

#### **Allowable subject matter**

Claims 8-10 are allowed.

The following is an examiner's statement of reasons for allowance:

The prior art neither discloses nor suggests the following limitations, in combination with the remaining elements as disclosed in claims 8 and 9:

- the first discharge being induced by said first pulse applied to the second electrode and which rises from a predetermined potential in the positive direction while the first electrode is set to a lower potential than the predetermined potential such as required by claim 8;
- the second pulse has a polarity opposite to the predetermined polarity of the first pulse with respect to the second potential and falls until reaching a potential that is lower than an unselected potential and higher than a

selected potential for performing the addressing discharge such as required by claim 9.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Claims 11 and 13 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

### **Conclusion**

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Asao et al. (6,160,529) discloses a method of driving plasma display panel. Kanazawa et al. (6,034,482) discloses a plasma display panel.

### **Correspondence**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Examiner Wilson Lee whose telephone number is (571) 272-1824.

Papers related to Technology Center 2800 applications may be submitted to Technology Center 2800 by facsimile transmission. Any transmission not to be considered an official response must be clearly marked "DRAFT". The official fax number is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Wilson Lee  
Primary Examiner  
U.S. Patent & Trademark Office

6/12/05